## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Art Unit:

Not assigned

Kazuko INUZUKA et al.

Examiner: Not assigned

Serial No: Not assigned

Filed: September 15, 2003

For: Synchronous Semiconductor Memory Device of Fast

Random Cycle System and Test Method Thereof

## TRANSMITTAL OF INFORMATION DISCLOSURE **STATEMENT**

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The information disclosure statement submitted herewith is being filed concurrently with the subject application [37 C.F.R. § 1.97(b)] and contains no items of information cited in any communication from a foreign patent office in a counterpart foreign application [37 C.F.R. § 1.97(e)(1)].

If it should be determined that for any reason either an insufficient or excessive fee has been paid, please charge any insufficiency or credit any overpayment necessary to ensure consideration of the information disclosure statement for the above-identified application to Deposit Account No. 50-1314. A copy of this paper is enclosed.

HOGAN &

Respectfully submitted,

Date: September 15, 2003

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FORM P10-1	FORM PTO-1449			Docket Number (Optional) 81790.0296		Application Number Not assigned	
INFORMATION DISCLOSURE CITATION IN AN APPLICATION			Applicant				
	(Use several sheets if necessary)		Filing Date September 1	Filing Date September 15, 2003		nit t assigned	
		U.S. PATEN	IT DOCUMENTS		<del></del>	<u></u>	
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
	6,426,915	07/30/02	OHSHIMA et al.				
	FC	DEFIGN PATI	ENT DOCUMENTS				
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